

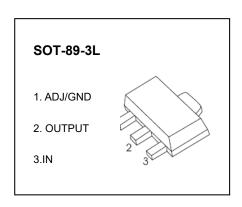
# JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD.

## 1A LOW DROPOUT LINEAR REGULATOR

# SCJA1117B-A Series

## **FEATURES**

- Low Dropout Voltage: 1.3V(typ.) at 0.8A Output Current
- Thermal Shutdown
- Three-Terminal Adjustable to 1.2V,1.8V, 2.5V, 3.3V, 5V
- Operation Junction Temperature: -40°C to 125°C
- Space-saving SOT-89-3L packages



## **GENERAL DESCRIPTION**

The SCJA1117B-A series is a group of low dropout three-terminal regulators with a dropout of 1.3V(typ.) at 0.8A output current.

The SCJA1117B-A series is available in an adjustable version, which can set the output voltage from 1.25V to 12V with only two external resistors. In addition, it is available in five fixed voltages: 1.2V, 1.5V, 1.8 V, 2.5 V, 3.3 V and 5 V.

The SCJA1117B-A series offer thermal shutdown protection. Its circuit includes a Zener trimmed bandgap reference to assure output voltage accuracy to within ±2%.

A minimum of 10µF tantalum capacitor is required at the output to improve the transient response and stability.

### **APPLICATIONS**

- PC Motherboard
- LCD Monitor
- Graphic Card
- DVD-Video Player
- NIC/Switch
- Telecom Modem
- ADSL Modem
- Printer and other peripheral Equipment

### **MARKING**



"A1117B": Device serial number.

"X.X": Output voltage, for example, if  $V_{OUT} = 3.0V$ , "X.X" = 3.0.

"YY": A code composed of one uppercase letter and one number, indicates weekly record information of production.

## **MAXIMUM RATINGS**

### **ORDERING INFORMATION**

Package	Operating Junction Temperature Range	Part NO.
		SCJA1117B-ADJ-A
		SCJA1117B-1.2-A
SOT-89-3L	-40 to 125°C	SCJA1117B-1.8-A
301-03-0E		SCJA1117B-2.5-A
		SCJA1117B-3.3-A
		SCJA1117B-5.0-A

# ABOSLUTE MAXIMUM RATINGS (T<sub>a</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Input Voltage	Vi	20	V
Thermal Resistance from Junction to Ambient	R <sub>eJA</sub>	250	°C/W
Operating Ambient Temperature	T <sub>A</sub>	-40~+85	℃
Maximum Junction Temperature	T <sub>j</sub>	-40~+150	°C
Storage Temperature	T <sub>stg</sub>	-40~+150	°C
Lead Temperature (Soldering, 10s)	TL	260	$^{\circ}$
ESD Rating	Human Body Model, HBM	8	kV

Note: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

#### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Value	Unit
Input Voltage	Vi	15	V
Operating Junction Temperature	Tj	-40~+125	${\mathfrak C}$

# **ELECTRICAL CHARACTERISTICS**

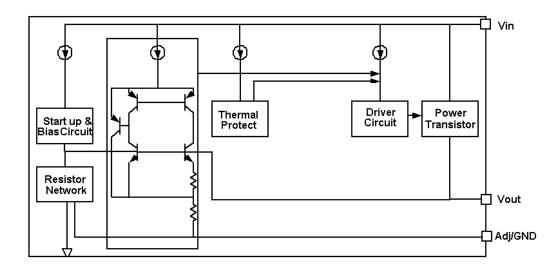
# T<sub>J</sub>=25℃ unless otherwise specified.

Parameter	Symbol	Part NO.	Test conditions	Min	Тур	Max	Unit	
Defense Veltere		00 1444470 AD LA	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =3.25V	1.225	1.25	1.275	.,	
Reference Voltage	V <sub>IROC</sub>	SCJA1117B-ADJ-A	10mA≤I <sub>OUT</sub> ≤1A, 2.75V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤12V	1.219	1.25	1.281	V	
		0011111777	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =3.2V	1.176	1.2	1.224		
		SCJA1117B-1.2-A	10mA≤I <sub>OUT</sub> ≤1A, 2.7V≤V <sub>IN</sub> ≤12V	1.170	1.2	1.230		
		0011111777	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =3.5V	1.470	1.5	1.530		
		SCJA1117B-1.5-A	10mA≤l <sub>OUT</sub> ≤1A, 3V≤V <sub>IN</sub> ≤12V	1.463	1.5	1.537		
		0011111777101	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =3.8V	1.764	1.8	1.836	V	
	.,	SCJA1117B-1.8-A	10mA≤l <sub>OUT</sub> ≤1A, 3.3V≤V <sub>IN</sub> ≤12V	1.755	1.8	1.845	Ĭ	
Output Voltage	Vo		I <sub>OUT</sub> =10mA, V <sub>IN</sub> =4.5V	2.450	2.5	2.550		
		SCJA1117B-2.5-A	10mA≤I <sub>OUT</sub> ≤1A, 4V≤V <sub>IN</sub> ≤12V	2.438	2.5	2.562		
			I <sub>OUT</sub> =10mA, V <sub>IN</sub> =5.3V	3.234	3.3	3.366		
		SCJA1117B-3.3-A	10mA≤I <sub>OUT</sub> ≤1A, 4.8V≤V <sub>IN</sub> ≤12V	3.218	3.3	3.382		
	İ		I <sub>OUT</sub> =10mA, V <sub>IN</sub> =7.0V	4.900	5.0	5.100		
		SCJA1117B-5.0-A	10mA≤I <sub>OUT</sub> ≤1A, 6.5V≤V <sub>IN</sub> ≤12V	4.875	5.0	5.125		
		SCJA1117B-ADJ-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤10.75V		0.03	0.2	%	
		SCJA1117B-1.2-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤8.8V		0.03	0.2		
	LNR	SCJA1117B-1.5-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤8.5V		0.03	0.2		
Line Regulation		SCJA1117B-1.8-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤10.2V		0.03	0.2	%/V	
		SCJA1117B-2.5-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤9.5V		0.03	0.2		
		SCJA1117B-3.3-A	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>IN</sub> -V <sub>OUT</sub> ≤8.7V		0.03	0.2		
		SCJA1117B-5.0-A	I <sub>OUT</sub> =10mA, 1.5V ≤V <sub>IN</sub> -V <sub>OUT</sub> ≤7V		0.03	0.2		
		SCJA1117B-ADJ-A			2	8	mV	
		SCJA1117B-1.2-A	$V_{\text{IN}}$ - $V_{\text{OUT}}$ =1.5V, 10mA $\leqslant$ I <sub>OUT</sub> $\leqslant$ 1A		2	8		
		SCJA1117B-1.5-A			2	8		
Load Regulation	LDR	SCJA1117B-1.8-A			3	12	mV	
		SCJA1117B-2.5-A			4	16		
		SCJA1117B-3.3-A			6	24		
		SCJA1117B-5.0-A			9	36		
Dropout Voltage	V <sub>D</sub>		I <sub>OUT</sub> =1A		1.3	1.5	V	
A.II. (D) C			V <sub>IN</sub> = 5V, I <sub>OUT</sub> =10mA		55	120	μA	
Adjust Pin Current	$I_{Adj}$	SCJA1117B-ADJ-A	V <sub>IN</sub> = 5V, I <sub>OUT</sub> =1A		55	120	μA	
I <sub>Adj</sub> change	I <sub>change</sub>	SCJA1117B-ADJ-A	V <sub>IN</sub> = 5V 10mA≤I <sub>OUT</sub> ≤1A		0.2	10	μA	
Minimum Load Current	IL IL	SCJA1117B-ADJ-A			2	10	mA	
		SCJA1117B-1.2-A	V <sub>IN</sub> = 10V		2	5	mA	
		SCJA1117B-1.5-A	V <sub>IN</sub> = 10V		2	5	mA	
		SCJA1117B-1.8-A	V <sub>IN</sub> = 12V		2	5	mA	
Quiescent Current	Iq	SCJA1117B-2.5-A	V <sub>IN</sub> = 12V		2	5	mA	
		SCJA1117B-3.3-A	V <sub>IN</sub> = 12V	1	2	5	mA	
		SCJA1117B-5.0-A	V <sub>IN</sub> = 12V	1	2	5	mA	
Ripple Rejection	RR		$f=1kHz, C_{IN}=10\mu F/25V, C_{OUT}=10\mu F/25V, V_{IN}-V_{OUT}=2V, I_{OUT}=10mA$		60		dB	

<sup>\*</sup> With package soldering to copper area over backside ground plane or internal power plane  $R_{\theta JA}$  can vary from 46 °C/W to >90°C/W depending on mounting technique and the size of the copper area

# **FUNCTIONAL BLOCK and TYPICAL APPLICATION**

#### **FUNCTIONAL BLOCK DIAGRAM**



#### **DETAILED DESCRIPTION**

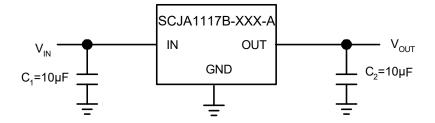
SCJA1117B-XXX-A is a series of low dropout voltage, three terminal regulators. Its application circuit is very simple: the fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, bandgap, thermal shutdown, power transistors and its driver circuit and so on.

The thermal shut down modules can assure chip and its application system working safety when the junction temperature is larger than 170°C.

The bandgap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The temperature coefficient is under 100 ppm/°C. And the accuracy of output voltage is guaranteed by trimming technique.

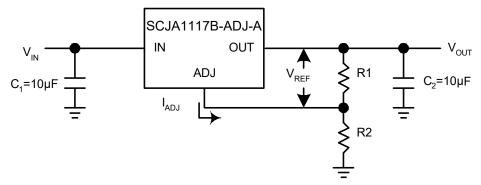
## **TYPICAL APPLICATION**

# **Fixed Output Voltage Version**



- 1) Recommend using 10uF tan capacitor as bypass capacitor (C1) for all application circuit.
- 2) Recommend using 10uF tan capacitor to assure circuit stability.

### **Adjustable Output Voltage Version**



VOUT = VREF× (1+ R2/R1)+IADJ×R2

The output voltage of adjustable version follows the equation: Vout=1.25×(1+R2/R1)+IAdj×R2. We can ignore IAdj because IAdj (about 50uA) is much less than the current of R1 (about 2~10mA).

- 1) To meet the minimum load current (>10mA) requirement, R1 is recommended to be 125ohm or lower. As SCJA1117B-ADJ-A can keep itself stable at load current about 2mA, R1 is not allowed to be higher than 625ohm.
- 2) Using a bypass capacitor ( $C_{ADJ}$ ) between the ADJ pin and ground can improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. The impedance of  $C_{ADJ}$  should be less than R1 to prevent ripple from being amplified. As R1 is normally in the range of  $100\Omega\sim500\Omega$ , the value of  $C_{ADJ}$  should satisfy this equation:  $1/(2\pi\times f_{ripple}\times C_{ADJ})<$ R1.

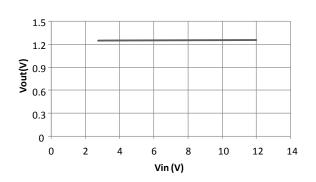
#### THERMAL CONSIDERATION

We have to take heat dissipation into great consideration when output current or differential voltage of input and output voltage is large. Because in such cases, the power dissipation consumed by SCJA1117B-ADJ-A is very large. SCJA1117B-ADJ-A series uses SOT-223 package type and its thermal resistance is about 20°C/W. And the copper area of application board can affect the total thermal resistance. If copper area is 5cm\*5cm (two sides), the resistance is about 30°C/W. So the total thermal resistance is about 20°C/W+30°C/W. We can decrease total thermal resistance by increasing copper area in application board. When there is no good heat dissipation copper are in PCB, the total thermal resistance will be as high as 120°C/W, then the power dissipation of SCJA1117B-ADJ-A could allow on itself is less than 1W. And furthermore, SCJA1117B-ADJ-A will work at junction temperature higher than 125°C under such condition and no lifetime is guaranteed.

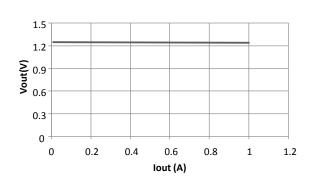
# TYPICAL PERFORMANCE CHARACTERISTICS

TA=25℃, unless otherwise noted.

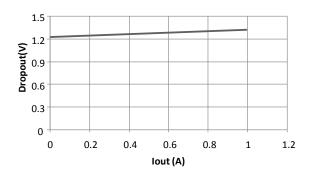
Line regulation



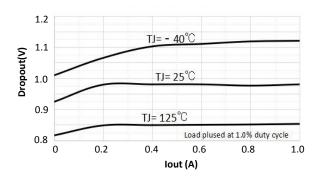
Load regulation



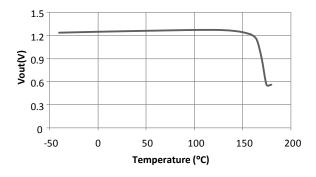
**Dropout Voltage (ADJ Except)** 



**Dropout Voltage (ADJ Only)** 



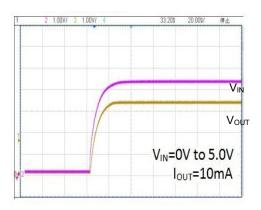
# Thermal performance with OTP



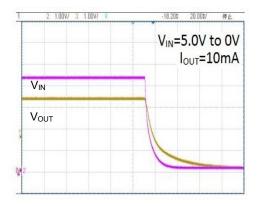
# TYPICAL PERFORMANCE CHARACTERISTICS

TA=25 $^{\circ}$ C, unless otherwise noted.

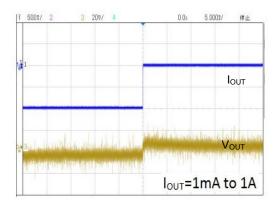
Power ON / OFF



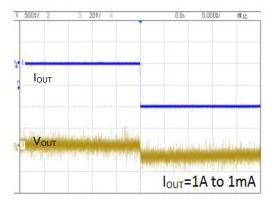
### Power ON / OFF



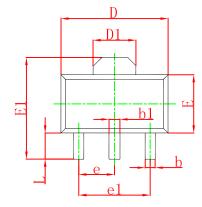
## **Load Transient Response**

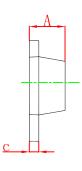


## **Load Transient Response**



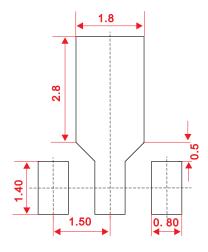
# **SOT-89-3L Package Outline Dimensions**





Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550	REF.	0.061 REF.		
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP.		0.060 TYP.		
e1	3.000 TYP.		0.118	TYP.	
L	0.900	1.200	0.035	0.047	

# **SOT-89-3L Suggested Pad Layout**



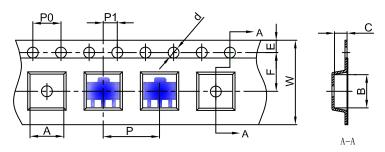
## Note:

- 1.Controlling dimension: in millimeters. 2.General tolerance:  $\pm$ 0.05mm.
- 3. The pad layout is for reference purposes only.

### **NOTICE**

JSCJ reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. JSCJ does not assume any liability arising out of the application or use of any product described herein.

# SOT-89-3L Embossed Carrier Tape

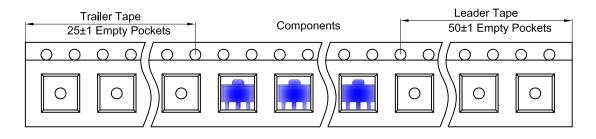


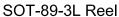
### Packaging Description:

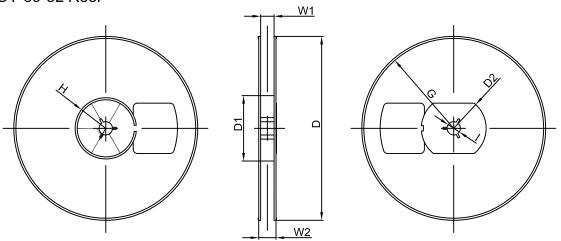
SOT-89-3L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 1,000 units per 7" or 18.0 cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

	Dimensions are in millimeter								
Pkg type A B C d E F P0 P P1 W							W		
SOT-89-3L									12.00

# SOT-89-3L Tape Leader and Trailer







	Dimensions are in millimeter								
Reel Option D D1 D2 G H I W1 W2							W2		
7"Dia									

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
1000 pcs	7 inch	10,000 pcs	203×203×195	40,000 pcs	438×438×220	

# **DISCLAIMER**

## IMPORTANT NOTICE, PLEASE READ CAREFULLY

The information in this data sheet is intended to describe the operation and characteristics of our products. JSCJ has the right to make any modification, enhancement, improvement, correction or other changes to any content in this data sheet, including but not limited to specification parameters, circuit design and application information, without prior notice.

Any person who purchases or uses JSCJ products for design shall: 1. Select products suitable for circuit application and design; 2. Design, verify and test the rationality of circuit design; 3. Procedures to ensure that the design complies with relevant laws and regulations and the requirements of such laws and regulations. JSCJ makes no warranty or representation as to the accuracy or completeness of the information contained in this data sheet and assumes no responsibility for the application or use of any of the products described in this data sheet.

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