# MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

IRLML6401

Product specification





### **Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-12V	40mΩ	-4.3A

#### **Features**

- -20V,-4.3A, RDS(ON)=40mΩ@VGS=-4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

# **Applications**

- Notebook
- Load Switch
- Hend-Held Instruments

#### **Reference News**

PACKAGE OUTLINE	P-Channel MOSFET	Marking
SOT-23	G	F ****

# Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
Vos	Drain-Source Voltage	-12	V
Vgs	Gate-Source Voltage	±12	V
	Drain Current – Continuous (Tc=25°C)	-4.3	А
lD	Drain Current – Continuous (Tc=100°C)	-3	А
Ірм	Drain Current – Pulsed1	-17.2	Α
	Power Dissipation (Tc=25°C)	1.56	W
PD	Power Dissipation – Derate above 25°C		W/°C
Тѕтс	Storage Temperature Range -55 to 150		°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
ReJA	Thermal Resistance Junction to ambient		80	°C/W



# Electrical Characteristics (T<sub>J</sub>=25 ℃, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	Vgs=0V , Ip=-250uA	-12			V
△BVɒss/△Tɹ	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =-1mA		-0.02		V/℃
Ipss	Drain-Source Leakage Current	$V_{DS}$ =-20 $V$ , $V_{GS}$ =0 $V$ , $T_{J}$ =25 $^{\circ}$ C			-1	uA
IDSS	Drain-Source Leakage Current	VDS=-16V , VGS=0V , TJ=125℃			-10	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		40	55	mΩ
T (DS(ON)	Statio Brain Source on Registaries	V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-2A		55	85	11122
V <sub>GS(th)</sub>	Gate Threshold Voltage	Vgs=Vps , Ip =-250uA	-0.4	-0.6	-1.0	V
$\triangle V$ GS(th)	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID230UA		2		mV/℃
gfs	Forward Transconductance	Vps=-10V , Is=-3A		7		S

## **Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>2,3</sup>		 9.6	
Qgs	Gate-Source Charge <sup>2,3</sup>	V <sub>DS</sub> =-10V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A	 1.6	 nC
Qgd	Gate-Drain Charge <sup>2, 3</sup>		 2	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>		 6	
Tr	Rise Time <sup>2 , 3</sup>	V <sub>DD</sub> =-10V , V <sub>GS</sub> =-4.5V ,	 21.6	 nS
$T_{d(off)}$	Turn-Off Delay Time <sup>2 , 3</sup>	R <sub>G</sub> =25Ω l <sub>D</sub> =-1A	 51	 113
Tf	Fall Time <sup>2, 3</sup>		 13.8	
Ciss	Input Capacitance		 850	
Coss	Output Capacitance	V <sub>DS</sub> =-10V , V <sub>GS</sub> =0V , F=1MHz	 70	 pF
Crss	Reverse Transfer Capacitance		 55	

### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-4.3	Α
Isм	Pulsed Source Current	To is or , reasonable			-8.6	Α
Vsp	Diode Forward Voltage	V <sub>G</sub> s=0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.2	V

#### Note

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%.
- ${\it 3. Essentially independent of operating temperature.}\\$

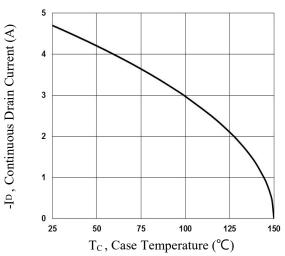


Fig.1 Continuous Drain Current vs. Tc

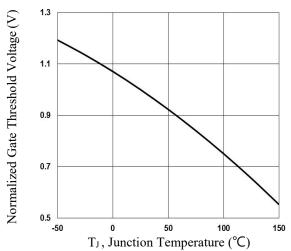


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

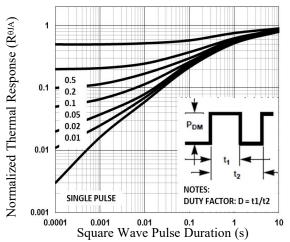


Fig.5 Normalized Transient Impedance

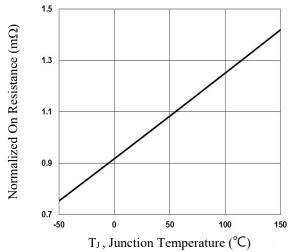


Fig.2 Normalized RDSON vs. T<sub>J</sub>

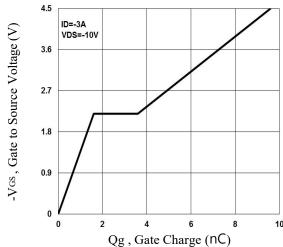


Fig.4 Gate Charge Waveform

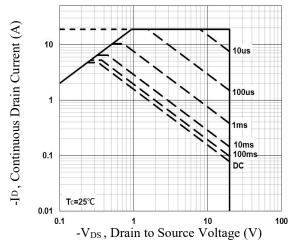
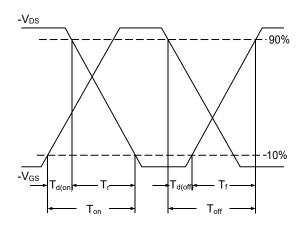
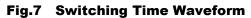


Fig.6 Maximum Safe Operation Area





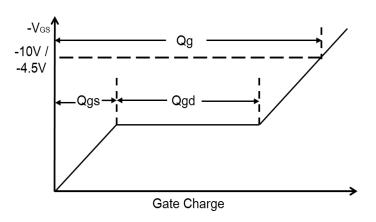
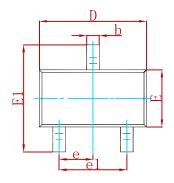
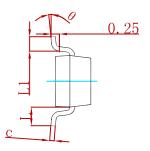


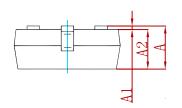
Fig.8 Gate Charge Waveform



# PACKAGE MECHANICAL DATA

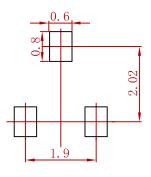






Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Зупьоі	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.95	0.950 TYP		7 TYP
e1	1.800	2.000	0.071	0.079
L	0.550	REF	0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

# **Suggested Pad Layout**



- 1.Controlling dimension:in millimeters. 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

# **REELSPECIFICATION**

P/N	PKG	QTY
IRLML6401	SOT-23	3000



#### **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.