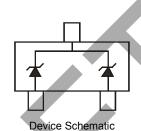
40W PEAK POWER DUAL SURFACE MOUNT TVS

Features

- Dual TVS in Common Cathode Configuration for ESD Protection
- 40 Watt Peak Power Dissipation @1.0ms (Unidirectional)
- 225mW Power Dissipation
- Ideally Suited for Automated Insertion
- Low Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3 & 4)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic.
 UL Flammability Rating Classification 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208 (3)
 Lead-Free Plating (Matte Tin Finish Annealed over Alloy 42
 Leadframe)
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)



SOT23



Top View

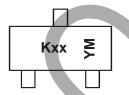
Ordering Information (Note 5 & 6)

Part Number	Compliance	Case	Packaging
MMBZ15VDL-7-F	Standard	SOT23	3000/Tape & Reel
MMBZ15VDLQ-7-F	Automotive	SOT23	3000/Tape & Reel

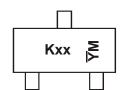
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Product manufactured with Date Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.
- 5. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
- 6. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



xx = Product Type Marking Code YM = Date Code Marking for Shanghai Assembly / Test site Y = Year (ex: I = 2021) M = Month (ex: 9 = September)



xx = Product Type Marking Code $\overline{Y}M$ = Date Code Marking for Chengdu Assembly / Test site \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007		2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	Т	U		G	Н	I	J	K	L	М	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Power Dissipation (Note 7)	P_{PK}	40	W

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 8)	P _D	225	mW
Thermal Resistance, Junction to Ambient Air (Note 8)	R _{OJA}	556	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

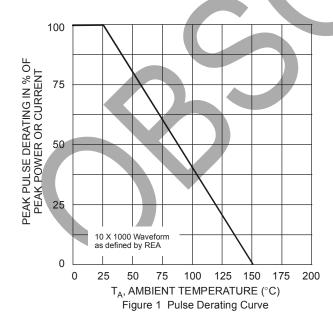
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

 $V_F = 0.9V \text{ max } @ I_F = 10mA$

Type Marking			Max Reverse	Breakdown Voltage				amping V _c @ I _{PP} te 7)	Typical Temperature	
Number	Code	V _{RWM}	Leakage I _R @ V _{RWM} (Note 9)	V	_{BR} (Note 9) (S	ھ ا	Vc	I _{PP}	Coefficient
		Volts	nA	Min	Nom	Max	mA	V	Α	T _c (%/°C)
MMBZ15VDL	KVJ	12.8	100	14.3	15	15.8	1.0	21.2	1.9	+0.080

Notes:

- Non-repetitive current pulse per Figure 2 and derate above T_A = +25°C per Figure 1.
 Device mounted on FR-5 PCB 1.0 × 0.75 × 0.062 inch pad layout as shown on Diodes Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com. 200mW per element must not be exceeded.
- 9. Short duration pulse test used to minimize self-heating effect



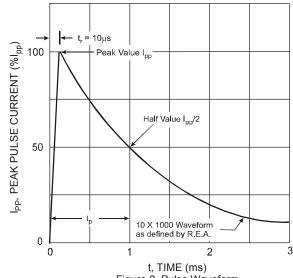
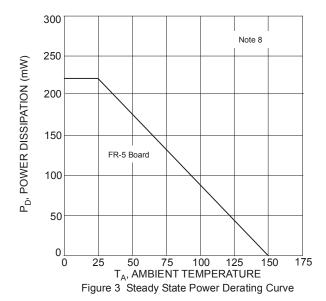


Figure 2 Pulse Waveform





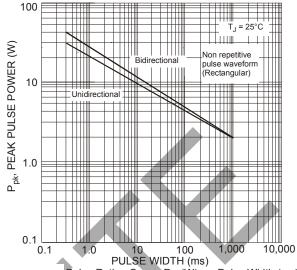
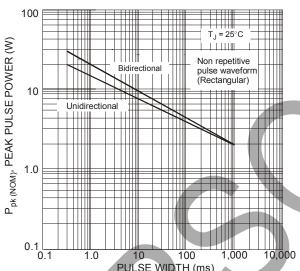


Figure 4 Pulse Rating Curve, $P_{pk}(W)$ vs. Pulse Width (ms) Power is defined as $P_{pk} = V_C \times I_{pp}$



PULSE WIDTH (ms)

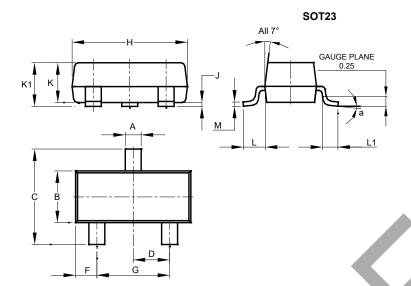
Figure 5 Pulse Rating Curve, $P_{pk (NOM)}(W)$ vs. Pulse Width (ms)

Power is defined as $P_{pk(NOM)} = V_{BR(NOM)} \times I_{pp}$ where $V_{BR(NOM)}$ is the nominal breakdown voltage



Package Outline Dimensions

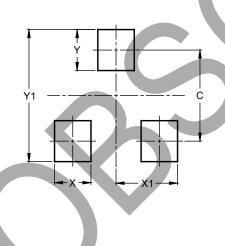
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1,03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
7	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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